

**PATENT** 

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Date Date Dodg

Ayesha J. Shaikh

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leel S. Janzen Attorney Docket No.: 501336.01

Serial No. : 10/713,587 Group Art Unit : 2116

Filed: November 13, 2003 Examiner: John R. Cottingham

Confirmation No. : 2001

Date of Notice

of Allowance : February 8, 2006

Title : APPARATUS AND METHOD FOR GENERATING A DELAYED CLOCK SIGNAL

Mail Stop Issue Fee Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

In the Examiner's reasons for allowance, he states each independent claim is allowable because the prior art does not teach or suggest a particular element or elements recited in each of the claims. While applicant agrees with the Examiner's indication that the prior art does not teach or disclose the particular elements cited by the Examiner, the undersigned would like to point out that each claim includes a combination of elements and it is the recited

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combination, which includes the elements cited by the Examiner, that is not disclosed nor suggested by the prior art.

While the combinations of elements recited in the allowed claims are allowable, the undersigned would like to point out that some or all of these individual elements may be broadened such that the resulting combination is still patentable. Applicant may elect to pursue such claims, or to pursue claims directed to other aspects of the present invention, through a continuation or reissue application, or through a reexamination proceeding.

Respectfully submitted,

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